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U.S. Serial No. 10/711,306

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Response to Restriction Requirement (2 pages)

Thank you. John H. Platt

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I hereby certify that this Response to Restriction Requirement is being facsimile transmitted to the United States Patent and Trademark Office at (571) 273-8300 and is addressed to: Mail Stop AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on:

Date: 1/26/06

-- PATENT --

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kenneth V. Buer

Docket No.:

36956.1317

Serial No.:

10/711,306

Confirmation

5305

No.:

Filing Date:

September 9, 2004

Examiner:

Lam T. Mai

Title:

OFF-CHIP BIAS FEED SYSTEM

Art Unit:

2819

## RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

### Honorable Commissioner:

In response to the Office Action mailed December 27, 2004, please accept this Response to the restriction requirement entered pursuant to 35 U.S.C. § 121 for the above-identified application.

Pursuant to 35 U.S.C. § 121, the Examiner entered a restriction requirement to one of the following inventions:

I:

Claims 1, 7-8, with respect to claims 1 and 7, the claims drawn to a connection of bond wire between matching structure and dc bias source.

II: Claims 2-6, with respect to claims 2, 3, 4, 5, the claims drawn to transistors of MMIC for receiving dc bias feed.

III. Claim 9-10, with respect to claim 9, the claim drawn to reduce size of MMIC relative to a conventional MMIC.

In response to the Examiner's restriction requirement, Applicant elects invention II, claims 2-6, without traverse, for further examination.

Applicant has timely filed this response which is due January 27, 2005. If any extension of time is required, please consider this a petition therefor and charge any additional fees which may be required to Deposit Account No. 19-2814. A duplicate copy of this sheet is enclosed.

Dated: January 26, 2005

Respectfully submitted,

John H. Platt Reg. No. 47,863

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II: Claims 2-6, with respect to claims 2, 3, 4, 5, the claims drawn to transistors of MMIC for receiving dc bias feed.

III. Claim 9-10, with respect to claim 9, the claim drawn to reduce size of MMIC relative to a conventional MMIC.

In response to the Examiner's restriction requirement, Applicant elects invention II, claims 2-6, without traverse, for further examination.

Applicant has timely filed this response which is due January 27, 2005. If any extension of time is required, please consider this a petition therefor and charge any additional fees which may be required to Deposit Account No. 19-2814. A duplicate copy of this sheet is enclosed.

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